# ECE 340 Homework

1. What is the difference between an *exception* and an *interrupt* (within the context of the PIC 32 and MIPS core)?
2. Briefly describe the sequence of events that occurs when an interrupt occurs.
3. What are the basic steps required to use a peripheral interrupt?
4. What is meant by *interrupt priorities* and when do they become relevant?
5. What is meant by *multivectored* interrupts? (Microchip does not recommend single vector)
6. What feature does Microchip provide in the PIC32 to reduce the latency associated with a context switch? *Hint: The PIC32MZ provides seven of them!*