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SDK C/C++ - design_1_wrapper_hw_platform_0/system.hdf - Xilinx SDK

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system.hdf system.mss

design_1_wrapper_hw_platform_0 Hardware Platform Specification

Design Information

Target FPGA Device: 7z020
Created With: Vivado 2014.4
Created On: Tue Apr 14 13:10:09 2015

Address Map for processor ps7_cortexa9_0

```
axi_gpio_0 0x41200000 0x4120ffff
ps7_af1_0 0xf8008000 0xf8008fff
ps7_af1_1 0xf8009000 0xf8009fff
ps7_af1_2 0xf800a000 0xf800afff
ps7_af1_3 0xf800b000 0xf800bfff
ps7_coresight_comp_0 0xf8800000 0xf88fffff
ps7_ddr_0 0x00100000 0x1fffffff
ps7_ddrc_0 0xf8006000 0xf8006fff
ps7_dev_cfg_0 0xf8007000 0xf8007fff
ps7_dma_ns 0xf8004000 0xf8004fff
ps7_dma_s 0xf8003000 0xf8003fff
ps7_ethernet_0 0xe000b000 0xe000bfff
ps7_globaltimer_0 0xf8f00200 0xf8f002ff
ps7_gpio_0 0xe000a000 0xe000afff
ps7_gpv_0 0xf8900000 0xf89fffff
ps7_intc_dist_0 0xf8f01000 0xf8f01fff
ps7_iop_bus_config_0 0xe0200000 0xe0200fff
ps7_l2cachec_0 0xf8f02000 0xf8f02fff
ps7_ocmc_0 0xf800c000 0xf800cfff
ps7_pl310_0 0xf8f02000 0xf8f02fff
ps7_pmu_0 0xf8891000 0xf8891fff
ps7_pmu_0 0xf8893000 0xf8893fff
ps7_qspi_0 0xe000d000 0xe000dfff
ps7_qspi_linear_0 0xfc000000 0xfcffffff
ps7_ram_0 0x00000000 0x00002ffff
ps7_ram_1 0xffff0000 0xfffffdfff
ps7_scuc_0 0xf8f00000 0xf8f000fc
ps7_scugic_0 0xf8f00100 0xf8f001ff
ps7_scugic_0 0xf8f01000 0xf8f01fff
ps7_scutimer_0 0xf8f00600 0xf8f0061f
ps7_scuwdt_0 0xf8f00620 0xf8f006ff
ps7_sd_0 0xe0100000 0xe0100fff
ps7_slcr_0 0xf8000000 0xf8000fff
ps7_ttc_0 0xf8001000 0xf8001fff
ps7_uart_1 0xe0001000 0xe0001fff
ps7_usb_0 0xe0002000 0xe0002fff
ps7_xadc_0 0xf8007100 0xf8007120
```

SDK C/C++ - LED_test_bsp/system.mss - Xilinx SDK

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system.hdf system.mss

LED_test_bsp Board Support Package

[Modify this BSP's Settings](#) [Re-generate BSP Sources](#)

Target Information

This Board Support Package is compiled to run on the following target.

Hardware Specification: F:\zedboard_test\project_1\project_1.sdk\design_1_wrapper_hw_platform_0\system.hdf
Target Processor: ps7_cortexa9_0

Operating System

Board Support Package OS.

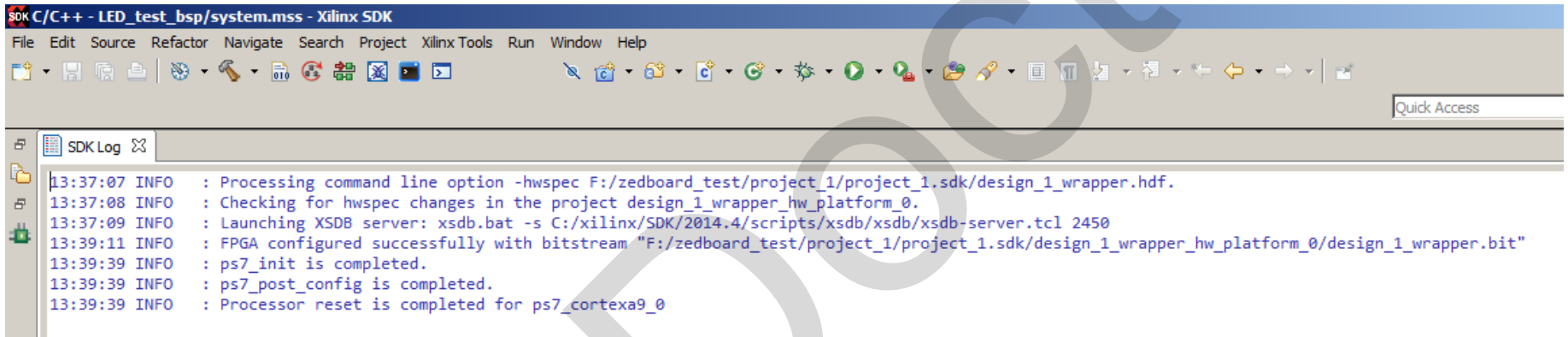
Name: standalone
Version: 4.2
Description: Standalone is a simple, low-level software layer. It provides access to basic processor features such as profiling, abort and exit.
Documentation: [standalone_v4_2](#)

Peripheral Drivers

Drivers present in the Board Support Package.

- axi_gpio_0 [gpio](#) [Documentation](#) [Import Examples](#)
- ps7_af1_0 [generic](#)
- ps7_af1_1 [generic](#)
- ps7_af1_2 [generic](#)
- ps7_af1_3 [generic](#)
- ps7_coresight_comp_0 [generic](#)
- ps7_ddr_0 [generic](#)
- ps7_ddrc_0 [generic](#)
- ps7_dev_cfg_0 [devcfg](#) [Documentation](#) [Import Examples](#)
- ps7_dma_ns [dmaps](#) [Documentation](#) [Import Examples](#)
- ps7_dma_s [dmaps](#) [Documentation](#) [Import Examples](#)
- ps7_ethernet_0 [emacps](#) [Documentation](#) [Import Examples](#)
- ps7_globaltimer_0 [generic](#)
- ps7_gpio_0 [gpiops](#) [Documentation](#) [Import Examples](#)
- ps7_gpv_0 [generic](#)
- ps7_intc_dist_0 [generic](#)
- ps7_iop_bus_config_0 [generic](#)
- ps7_l2cachec_0 [generic](#)
- ps7_ocmc_0 [generic](#)
- ps7_pl310_0 [generic](#)
- ps7_pmu_0 [generic](#)
- ps7_qspi_0 [qspips](#) [Documentation](#) [Import Examples](#)
- ps7_qspi_linear_0 [generic](#)
- ps7_ram_0 [generic](#)
- ps7_ram_1 [generic](#)
- ps7_scuc_0 [generic](#)
- ps7_scugic_0 [scugic](#) [Documentation](#) [Import Examples](#)

Overview Source



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Quick Access

SDK Log

```
13:37:07 INFO : Processing command line option -hwspec F:/zedboard_test/project_1/project_1.sdk/design_1_wrapper.hdf.
13:37:08 INFO : Checking for hwspec changes in the project design_1_wrapper_hw_platform_0.
13:37:09 INFO : Launching XSDB server: xsdb.bat -s C:/xilinx/SDK/2014.4/scripts/xsdb/xsdb/xsdb-server.tcl 2450
13:39:11 INFO : FPGA configured successfully with bitstream "F:/zedboard_test/project_1/project_1.sdk/design_1_wrapper_hw_platform_0/design_1_wrapper.bit"
13:39:39 INFO : ps7_init is completed.
13:39:39 INFO : ps7_post_config is completed.
13:39:39 INFO : Processor reset is completed for ps7_cortexa9_0
```